

TITLE: Color Image reading apparatus with plural linear sensors
which can read different lines of the image

Detailed Description Text - DETX (6):

The structure will now be explained in further detail. As shown in FIG. 2, the contact type color CCD sensor unit 11 is composed of five CCD chips 21-25 arranged in staggered fashion on a ceramic substrate 26, a cover 27 for substrate 26 and connecting flexible cables 28a-28f. In the CCD chips 21-25, each photosensor element is composed of a p-n photodiode of a size of 62.5 μ m \times 15.5 μ m. Each CCD chip is provided, as shown in FIG. 4, with photosensor elements of 3,168 bits, including 12 bits of empty pixels D1-D12 unconnected with photosensitive elements, 24 bits of light-shielded pixels D13-D36 provided with an aluminum shield, 36 bits of dummy pixels D37-D72, 3,072 bits of effective pixels S1-S3072, and 24 bits of rear dummy pixels D73-D96.

Patent Number:	4,891,690
Date of Patent:	Jan. 2, 1990

DESIGN PATENT DOCUMENTS

6/1/84	Supervisors Fed. CR	152/79
6/1/84	Fed. Rep. of Germany	152/79
10/1/82	Japan	152/79
10/1/82	Japan	152/79
6/1/82	Japan	152/79
6/1/82	Japan	152/79
6/1/84	Japan	152/79
6/1/82	Japan	152/79
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OTHER PUBLICATIONS

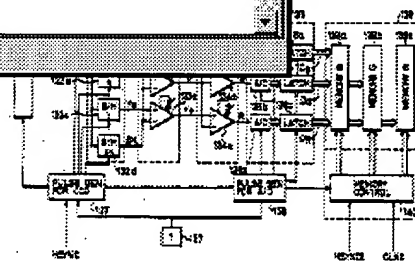
min. Elektrolyse Ziffernheftung 120
Perz - Straße, Boston Federal Reprint
1942, pp. 28-31.

Editor—James J. Grogan
Editor—Randall S. O'Brien
Editor—Blair G. Cella

ABSTRACT

A reading apparatus includes a plurality of such capable of generating plural analog signals, the elements are arranged in the order that is a divided manner as in the order of the inner sensors read out in lines of the color image. Also provided is separating the plural analog color components generated by the sensors into analog signals of each color, and a converter for converting the analog color component signals of each color into digital color component signals of each color. The apparatus further includes a mixing stage producing digital color signals representing a line of the color image, and color component signals of each color of the converter. In addition, a processor is provided for processing of the plural color component signals to form digital color signals for the reconstruction of the color image.

1 Cedar, 18 Douglas, 5 Spruce



11	US 5452001 A
12	US 5038225 A
13	US 4891690 A
14	US 4492980 A

US-PAT-NO: 5612739

DOCUMENT-IDENTIFIER: US 5612739 A

TITLE: Charge transfer device

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Detailed Description Text - DETX (22):

Accordingly, in the charge transfer device of this embodiment charge is outputted after the pixel signal charges have been output. It should be noted that the shielded photosensor 1a in the sensor the closest to the output end contributes to outputting OPB (0). Accordingly, it is possible to detect a dark component.

Details Text Image HTML KWIC

6 US 5633679 A

7 US 5612739 A

8 US 5591660 A

9 US 5567575 A

4

United States Patent (19)

Maki et al.

US 5612739 A

Patent Number: 5,612,739

Date of Patent: Mar. 18, 1997

(4) CHARGE TRANSFER DEVICE

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(31) Assignee: Sony Corporation, Tokyo, Japan

(37) Appl. No.: 820,677

(32) Filed: Mar. 7, 1996

Related U.S. Application Data

(37) Continuation of Ser. No. 118,842, Mar. 23, 1994, now

abandoned.

(37) Foreign Application Priority Data

Mar. 13, 1995 (JP) Japan 5-068211

(31) Int. Cl.⁶ H04N 03/34

(32) U.S. Cl. 348/311; 348/343; 348/351

(51) Field of Search 348/311, 343,

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US-PAT-NO: 5717459

DOCUMENT-IDENTIFIER: US 5717459 A

TITLE: Solid state imager device having A/D converter

DATE-ISSUED: February 10, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	C
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ASSIGNEE INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUN
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Nikon Corporation	Tokyo	N/A	N/A	JP

APPL-NO: 08/ 639733

DATE FILED: April 29, 1996

Details Text Image HTML FRO

60 US 5726710 A

61 US 5719625 A

62 US 5717459 A

63 US 5717457 A

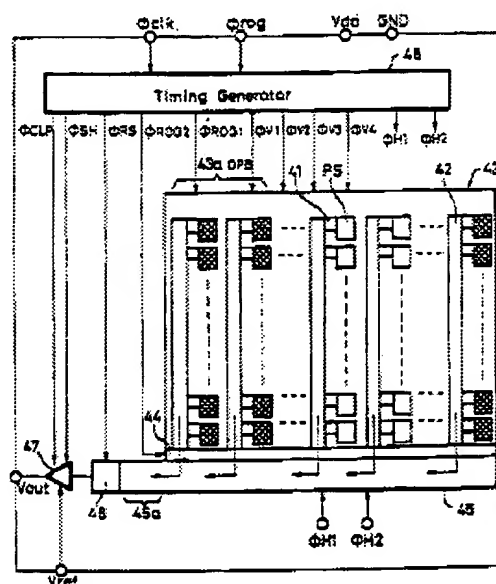
U.S. Patent

Feb. 10, 1998

Sheet 5 of 8

5,717,459

FIG. 5



DERWENT-ACC-NO: 1995-258846

DERWENT-WEEK: 200264

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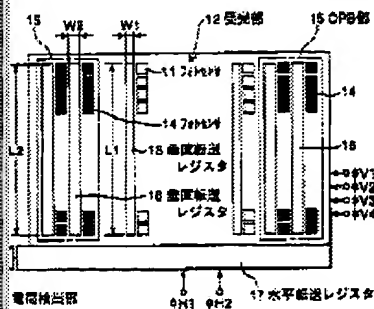
TITLE: Solid-state image pick-up element e.g. CCD - s
wider channel width for OPB transmission register
compared to channel width of light transmission

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Basic Abstract Text - ABTX (1):

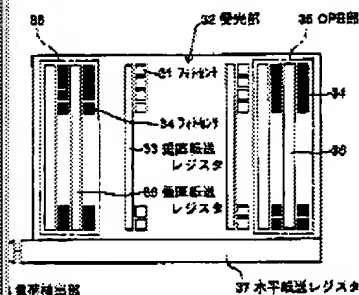
The image pick-up element is equipped with a light receiver (11). The photosensor carries out photo conversion of the incident light. The image pick-up element is an OPB (optical black part, 15) which consists of a shaded photo

【図1】



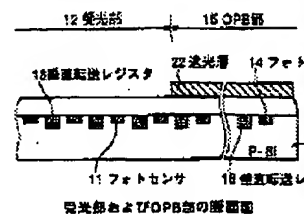
本発明の一実施例を示す概略構成図

【図3】



従来例を示す概略構成図

【図2】



受光部およびOPB部の断面図

Details Text Image HTML KWIC

20 EP 1058313 A

21 JP 10246879 A

22 JP 07161960 A

23 JP 02260868 A

【修正書】

[illegible]

US-PAT-NO: 6046823

DOCUMENT-IDENTIFIER: US 6046823 A

See image for Certificate of Correction

TITLE: Interface control for analog signal processing

----- KWIC -----

Detailed Description Text - DETX (3):

The interface used in the image processing system in this invention is shown in FIG. 2 as the scanning timing control, the optical black pixel timing for the clamp and Sample-and-Hold. System controller uses the OE (Output Enable) signal to distinguish bus input/output direction and the synchronization signal of the line. When OE signal is high, the system controller can control signal processor through the data bus D0-D5, while the image sensor residue analog image charge. When OE is low, the data bus D0-D5 output port to output the digital image data to the system controller. Meanwhile, the image sensor starts to expose the document line the analog pixel signal of the last exposed line to an analog signal processor.

Details Text Image HTML KWIC

2 US 6118115 A

3 US 6046823 A

4 US 5995197 A

5 US 5748335 A

United States Patent
Chen

Patent Number: 6,046,823
Date of Patent: Apr. 4, 2000

[54] INTERFACE CONTROL FOR ANALOG SIGNAL PROCESSING

[57] Inventor: Michael Chen, Hsin-Chi, Taiwan

[58] Assignee: Avision Inc., Hsin-Chi, Taiwan

[61] Appl. No.: 83/041,348

[62] Filed: Mar. 13, 1999

[63] Int. Cl. 7: H04N 1/00

[64] U.S. Cl.: 348/400, 348/402

[66] Field of Search: 348/400, 348/402, 348/404, 348/405, 348/406, 348/407, 348/408, 348/409, 348/410, 348/411, 348/412, 348/413, 348/414, 348/415, 348/416, 348/417, 348/418, 348/419, 348/420, 348/421, 348/422, 348/423, 348/424, 348/425, 348/426, 348/427, 348/428, 348/429, 348/430, 348/431, 348/432, 348/433, 348/434, 348/435, 348/436, 348/437, 348/438, 348/439, 348/440, 348/441, 348/442, 348/443, 348/444, 348/445, 348/446, 348/447, 348/448, 348/449, 348/450, 348/451, 348/452, 348/453, 348/454, 348/455, 348/456, 348/457, 348/458, 348/459, 348/460, 348/461, 348/462, 348/463, 348/464, 348/465, 348/466, 348/467, 348/468, 348/469, 348/470, 348/471, 348/472, 348/473, 348/474, 348/475, 348/476, 348/477, 348/478, 348/479, 348/480, 348/481, 348/482, 348/483, 348/484, 348/485, 348/486, 348/487, 348/488, 348/489, 348/490, 348/491, 348/492, 348/493, 348/494, 348/495, 348/496, 348/497, 348/498, 348/499, 348/500, 348/501, 348/502, 348/503, 348/504, 348/505, 348/506, 348/507, 348/508, 348/509, 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348/1558, 348/1559, 348/1560, 348/1561, 348/1562, 348/1563, 348/1564, 348/1565, 348/

7 Claims, 34 Examples & Drawings

DOCUMENT-IDENTIFIER: US 20020001037 A1

TITLE: PHOTOELECTRIC CONVERSION DEVICE PROVIDING
READOUT OF TWO-DIMENSIONAL ARRAY OF TRANSDUCERS

----- KWIC -----

Current US Classification, US Primary Class/Subclass - CCPR (1)
348/302

Detail Description Paragraph - DETX (17):

[0098] As another application, the sensor according to the prior art is greatly useful in detection of opening and closing of the eyes of an automobile driver. An external LED light is irradiated to the eye according to the above method and the reflected light is received by the sensor of the invention. Though a high intensity signal can be detected when a driver is awake, the intensity of the reflected light lowers when a driver is sleepy and a time of the closed eyes is long. When the sensor output is lower than the average intensity of the reflected light, a sleepiness preventing apparatus including a buzzer is activated.

Details Text Image HTML KWIC

9 US 20020024605 A1

10 US 20020012056 A1

11 US 20020001037 A1

12 US 20010040633 A1



(1) United States

(2) Patent Application Publication
MIYAWAKI et al.(3) Pub. No.: US 2002/0001037 A1
(4) Pub. Date: Jan. 3, 2002(5) PHOTOELECTRIC CONVERSION DEVICE
PROVIDING ADVANTAGEOUS READOUT
OF TWO-DIMENSIONAL ARRAY OF
TRANSDUCERS

Related U.S. Application Data

(6) Continuation of application No. 08/249,928, filed on
Mar. 24, 1994, now abandoned.(7) Inventors: MAMORU MIYAWAKI
KEIICHI MIYAKI
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(8) Foreign Application Priority Data

May 28, 1993 (JP) 4-227687
Aug. 6, 1993 (JP) 4-228607Correspondence Address:
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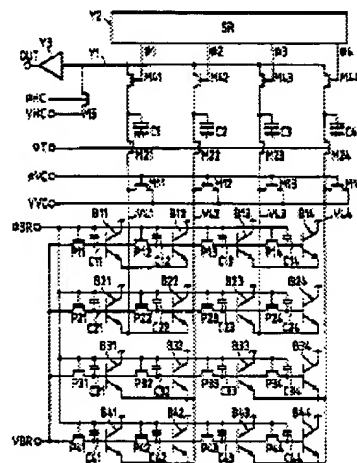
Publication Classification

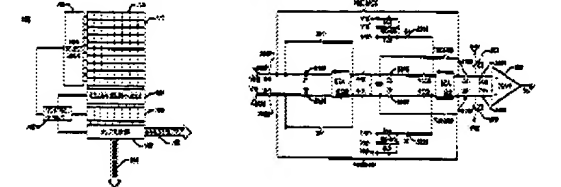
(9) Int. Cl. H01N 3/14; H01N 3/30
(10) U.S. Cl. 254/248; 254/252(11) Notice: This is a publication of a patented pre-
sented application (CIPA) filed under 37
C.F.R. 1.51(a).

(12) Appl. No. 08/249,928

(13) Filed: Mar. 24, 1994

ABSTRACT

The present invention is directed to provide a photoelectric
conversion device provided with a plurality of groups of
photoelectric conversion elements, wherein the plurality of groups are respectively
provided with a detection circuit for detecting a peak signal
of each group.



US-PAT-NO: 6320617

DOCUMENT-IDENTIFIER: US 6320617 B1

See image for Certificate of Correction

TITLE: CMOS active pixel sensor using a pinned photo

----- KWIC -----

Detailed Description Text - DETX (39):

Next, the clamped reset voltage is turned off, and the "crowt" is turned on at 810. The clamped reset voltage V_{cl} is controlled by the voltage CL that drives the clamping transistors 442 and 45. switch 428 is turned on, the inputs to the p-channel source-follower are shorted together. This averages the charge and also captures p-channel source followers in the column being read out.

Current US Original Classification - CCOR (1):

348/302

Details Text Image HTML KWIC

26 US 6445413 B1

27 US 6417882 B1

28 US 6320617 B1

29 US 6166367 A

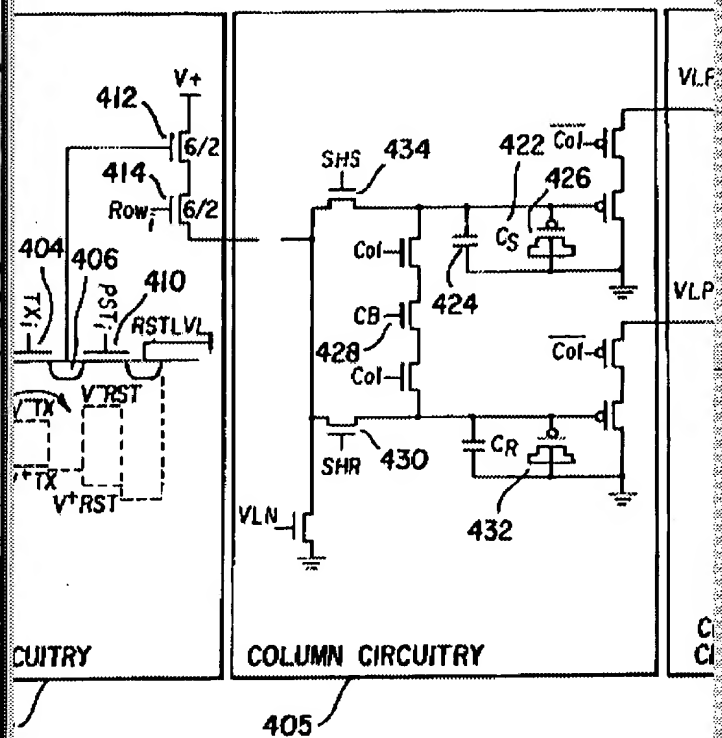


FIG. 4

made over either a full image area (average mode) or a restricted mode). The present invention is easiest to embody in a full image with wiring changes in the array could be adapted to operate in

(19) United States
(12) Patent Application Publication (22) Pub. No.: US 2002/0024605 A1
(35) Pub. Date: Feb. 28, 2002

(36) METHOD FOR CAPTURING A DARK
FRAME

PHOTON CAMERAS

(73) Inventors: Richard B. Merrill, Woodside, CA
(US) Carver A. Moss, Cupertino, CA
(US) Richard E. Lavoie, Los Altos, CA
(US)

(51) Int. Cl.⁷ H04N 3/34
(52) U.S. Cl. 348/240; 348/252

(57) ABSTRACT

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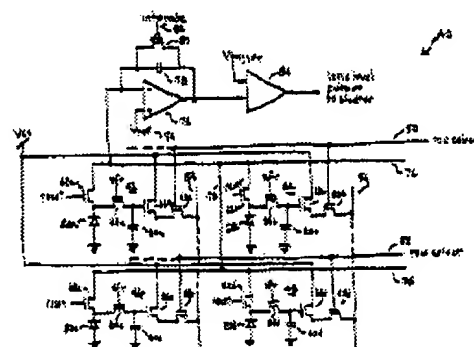
(21) Appl. No.: 09/872,996

(22) Filed: May 21, 2002

Related U.S. Application Data

(26) Division of application No. 09/031,255, filed on Feb. 16, 1998.

A method for controlling the sequence of an active pixel array detector and camera includes the steps of: integrating photocurrent in each pixel during an integration time period; selecting overflow charge from all pixels in the array during the integration time period; developing an overflow signal as a function of the overflow charge; and normalizing the integration time period when the overflow signal exceeds a preset threshold level selected to represent a desired reference exposure level. Apparatus for performing the method of the present invention includes circuitry for integrating photocurrent in each pixel during an integration time period; circuitry for detecting and detecting overflow charge from all pixels in the array during the integration time period; circuitry for developing an overflow signal as a function of the overflow charge; and circuitry for normalizing and integration time period when the overflow signal exceeds a preset threshold level selected to represent a desired reference exposure level.



Details Text Image HTML KWIC

7 US 20020085109 A1

8 US 20020024606 A1

9 US 20020024605 A1

10 US 20020012056 A1

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DOCUMENT-IDENTIFIER: US 20010030702 A1**TITLE: Video bus for high speed multi-resolution image method thereof**

----- KWIC -----

**Current US Classification, US Secondary Class/Subclass - C C S F
348/302****Detail Description Paragraph - DETX (27):**

[0058] Another useful approach to the column parallel nature 202(1) with the bus system 200(1) in accordance with the present ability to select multiple columns at once to average the video. This is possible because the column amplifiers in the processing the pixel array 204 are identical in every detail and when more selected at once, the outputs from each amplifier try to drive the result is that the two or more signals are averaged. This at higher speed of operation and it also gives a new method of binning interpolating pixels. Binning is a term used to combine two or

Details Text Image HTML KWIC

13 US 20010040631 A1

14 US 20010030702 A1

15 US 20010026321 A1

16 US 20010012070 A1



US 20010030702 A1

(19) United States
(12) Patent Application Publication (13) Pub. No.: US 2001/0030702 A1
(11) Zarnowski et al. (14) Pub. Date: Oct. 18, 2001

(51) VIDEO BUS FOR HIGH SPEED
MULTI-RESOLUTION IMAGES AND
METHOD THEREOF

of application No. 09/036430, filed on Mar. 16, 1994,
now Pat. No. 5,624,125.

Publication Classification

(72) Inventors: Jeffrey Zarnowski, Melrose, NY (US);
Matthew Pata, Cortland, NY (US);
Thomas Vagstad, Cortland, NY
(US); Michael Jopson, North Syracuse,
NY (US)

(52) Int. Cl. H04N 2/64; H04N 5/115
(53) U.S. Cl. 348/302; 348/303

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Rochester, NY 14603 (US)

(21) Appl. No.: 09/768,134

(22) Filed: Jan. 23, 2001

Related U.S. Application Data

(63) Continuation-in-part of application No. 09/470,874,
filed on Jan. 24, 2000, which is a continuation-in-part

ABSTRACT

A two system and an image for transferring signals from a plurality of signal sources to an output includes a plurality of signal buses in parallel and a control system. The control system multiplexes the signals from two or more of the plurality of signal sources into one or more of the plurality of signal buses and allows the signals to substantially change each of the one or more of the plurality of signal buses before demultiplexing the signals to the output. A method for transferring signals includes multiplexing signals on to two or more of a plurality of signal buses and allowing the signals to substantially change each of the one or more of the plurality of signal buses before demultiplexing the signals to the output.

US-PAT-NO: 5933190

DOCUMENT-IDENTIFIER: US 5933190 A

TITLE: Pixel structure, image sensor using such pixel and corresponding peripheral circuitry

----- KWIC -----

Detailed Description Text - DETX (22):

It is a further object of the present invention to provide an electronic circuit wherein certain error signals, known as so-called "white" errors, are corrected by an averaging or minimization operation on a group of pixels in the same area. An implementation is shown in FIG. 7. A group of pixels 70, 71, 72 and 73 is treated as one group for signal processing. According to the implementation shown, a kind of minimum or average value of the 4 pixels will be used for further processing. If one of the pixels, for example pixel 70, has a defective (leaking) photodiode, during the addressing of the pixel, switches 74 and 75 will be closed and the output signal of the pixel will be replaced by an appropriate average value of the pixels 71, 72 and 73. It is evident that any other implementation of such correction method is possible.

Details Text Image HTML KWIC

31 US 5978025 A

32 US 5973341 A

33 US 5933190 A

34 US 5933189 A

U.S. Patent

Aug. 3, 1999

Sheet 8 of 7

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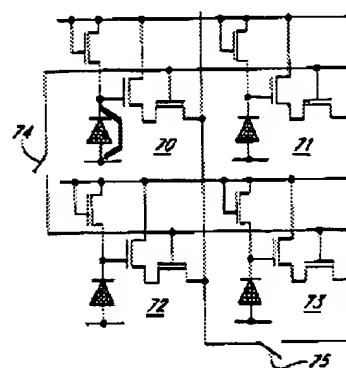


FIG. 7

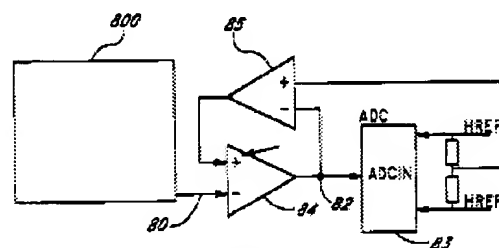


FIG. 8